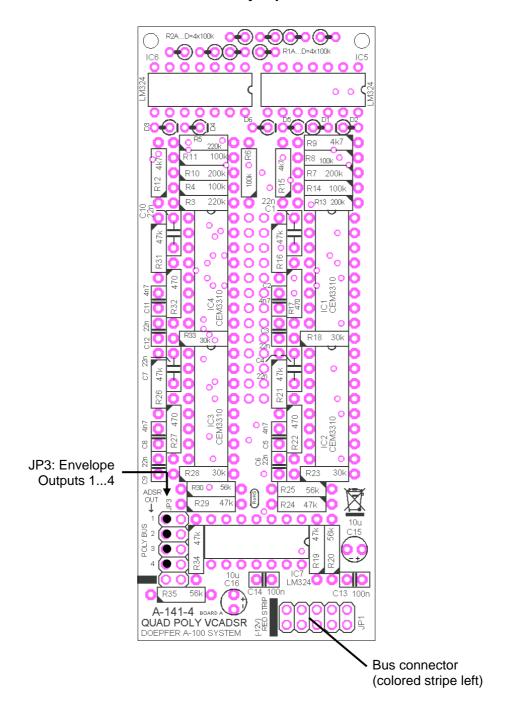
DDEPFER MUSIKELEKTRONIK GMBH ANALOG MODULAR SYSTEM A-100

A-141-4 Quad Polyphonic VCADSR

Position and function of the jumpers and connectors Board A



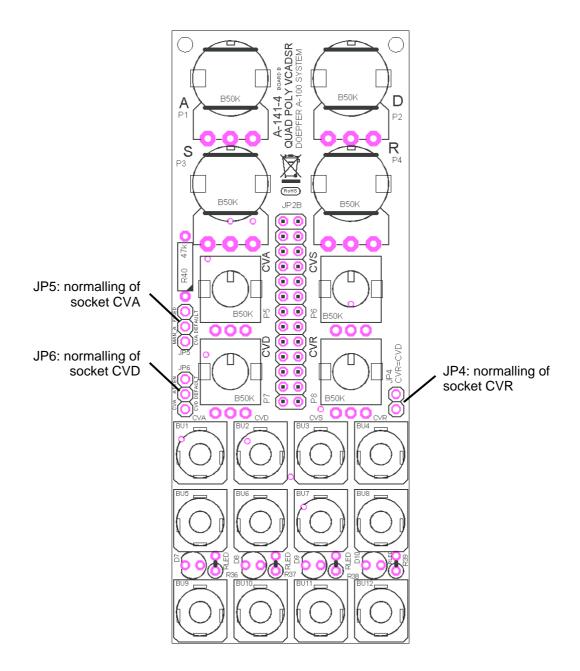
Function of JP3:

JP3 may be used for the internal connection of the envelope outputs to other modules (e.g. to the default CV input terminals of the polyphonic filter module A-105-4 or the default CV input terminals of the polyphonic VCA module A-132-8, typically one A-141-4 is used to control the VCF and another A-141-4 to control the VCA).

For the internal wiring of the pins of JP3 single wire cables with female connectors on both sides can be used (available in electronic shops, e.g. used for Arduino or Raspberry Pi boards). Even 10 pin female IDC connectors with ribbon cables may be used if one pays attention to the correct polarity.

A-141-4 Quad Polyphonic VCADSR

Position and function of the jumpers and connectors Board B



The pin headers JP4, JP5 and JP6 may be used for the normalling of the sockets CVA, CVD and CVR

JP5

jumper in upper position \rightarrow socket CVA is normalled to about +5V (fixed voltage) jumper in lower position \rightarrow socket CVA is normalled to the voltage generated by control "A" no jumper \rightarrow no normalling (factory setting)

JP6:

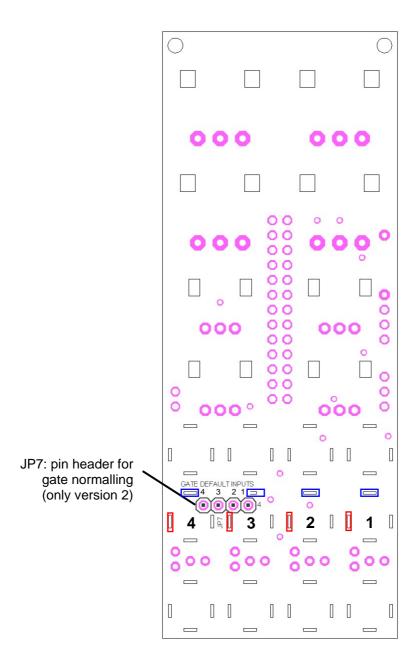
jumper in upper position → socket CVD is normalled to attenuated CVA (i.e. post CVA attenuator) jumper in lower position → socket CVD is normalled to socket CVA (i.e. pre CVA attenuator) no jumper → no normalling (factory setting)

.IP4

jumper installed → socket CVR is normalled to socket CVD no jumper → no normalling (factory setting)

The idea behind the normalling options is the possibility to change all time parameters (A, D, R) simultaneously by one CV or control only. If e.g. JP5 and JP6 are equipped with jumpers in the upper position and the jumper of JP4 is installed the CVA control can be used to change all three time parameters A, D and R simultaneously. If the jumper of JP5 is in the lower position also main control A can be used for that. Without a jumper on JP5 the CVA socket can be used to apply an external control voltage to control all three time parameters simultaneously.

A-141-4 Quad Polyphonic VCADSR Normalling of the Gate inputs (board B)



<u>Version 1</u> of the module is not equipped with pin headers for gate normalling. Rather the terminals of switching contacts of the gate sockets have to be used directly (i.e. the wires have to be soldered directly to the solder pins name 1...4 and marked with red rectangles).

<u>Version 2</u> of the module is equipped with an angled pin header (JP7) for the normalling of the gate inputs. It can be used to connect cables with female connectors (as mentioned before). Pay attention that the labelling on the pc board for JP7 is wrong: the correct sequence is 4-3-2-1 rather that 1-2-3-4 from left to right.

The terminals which correspond to the gate inputs are marked with blue rectangles. These can be used to normalize the gates $1 \rightarrow 2 \rightarrow 3 \rightarrow 4$. For this the gate input of the preceding gate socket has to be wired to the switching contact of the subsequent gate socket, e.g. blue #1 \rightarrow red #2, blue #2 \rightarrow red #3, blue #3 \rightarrow red #4 for the complete normalling $1 \rightarrow 2 \rightarrow 3 \rightarrow 4$.